

# N-CHANNEL SILICON FIELD EFFECT TRANSISTORS

## BFW10 BFW11

N-Channel depletion mode silicon epitaxial planar junction field effect transistors designed for use in wide-band amplifiers (0 to 300MHz). Their very low noise figure at low frequencies makes them suitable for differential amplifiers, electromedical and nuclear detector pre-amplifiers. They are in TO-72 encapsulation with a shield lead connected to case.

### QUICK REFERENCE DATA

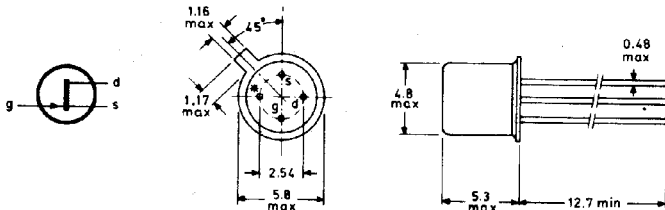
$V_{DS}$ max.	30	V	
$-V_{GS0}$ max. (open drain)	30	V	
$P_{tot}$ max. ( $T_{amb} < 25^{\circ}C$ )	300	mW	
	BFW10	BFW11	
$I_{DSS}$ ( $V_{DS} = 15V, V_{GS} = 0$ ) min.	8.0	4.0	mA
max.	20	10	mA
$-V_{(P)GS}$ max. ( $I_D = 0.5nA, V_{DS} = 15V$ )	8.0	6.0	V
$-C_{rs}$ max. ( $f = 1.0MHz, V_{DS} = 15V,$ $V_{GS} = 0$ )	0.80	0.80	pF
$ y_{fs} $ min. ( $f = 200MHz, V_{DS} = 15V,$ $V_{GS} = 0$ )	3.2	3.2	mA/V
N max. ( $f = 100MHz, V_{DS} = 15V,$ $V_{GS} = 0, R_G = 1k\Omega$ )	2.5	2.5	dB
$V_{\pi}/\sqrt{B}$ max. ( $f = 10Hz, B = 5.0Hz$ )	75	75	nV/ $\sqrt{Hz}$

Unless otherwise stated, data are applicable to both types

### OUTLINE AND DIMENSIONS

Conforms to J. E. D. E. C. TO-72

B.S. 3934 SO-12A/SB4-3



All dimensions in mm

Insulated electrodes \ \*Shield lead (connected to case)

Accessories available: 56246, 56263

D2967

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## RATINGS

Limiting values of operation according to the absolute maximum system.

### Electrical

$\pm V_{DS}$ max.	Drain-source voltage ( $V_{GS} = 0$ )	30	V
$V_{DGO}$ max.	Drain-gate voltage (open source)	30	V
$-V_{GSO}$ max.	Gate-source voltage (open drain)	30	V
$I_D$ max.	Drain current	20	mA
$I_G$ max.	Gate current	10	mA
$P_{tot}$ max.	Power dissipation ( $T_{amb} \leq 25^\circ\text{C}$ )	300	mW

### Temperature

$T_{stg}$ min.	-65	$^\circ\text{C}$
$T_{stg}$ max.	200	$^\circ\text{C}$
$T_j$ max.	200	$^\circ\text{C}$

### THERMAL CHARACTERISTIC

$R_{th(j-amb)}$	0.59	$^\circ\text{C}/\text{mW}$
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### ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ\text{C}$ unless otherwise stated)

		BFW10	BFW11	
$-I_{GSS}$	Gate cut-off current			
	$-V_{GS} = 20\text{V}, V_{DS} = 0$	max. 0.1	0.1	nA
$I_{DSS}$	$-V_{GS} = 20\text{V}, V_{DS} = 0,$ $T_j = 150^\circ\text{C}$	max. 0.5	0.5	$\mu\text{A}$
	*Drain current $V_{DS} = 15\text{V}, V_{GS} = 0$	min. 8.0 max. 20	4.0 10	mA mA
$-V_{GS}$	Gate-source voltage $I_D = 400\mu\text{A}, V_{DS} = 15\text{V}$	min. 2.0	-	V
		max. 7.5	-	V
	$I_D = 50\mu\text{A}, V_{DS} = 15\text{V}$	min. -	1.25	V
		max. -	4.0	V

\*Measured under pulsed conditions.

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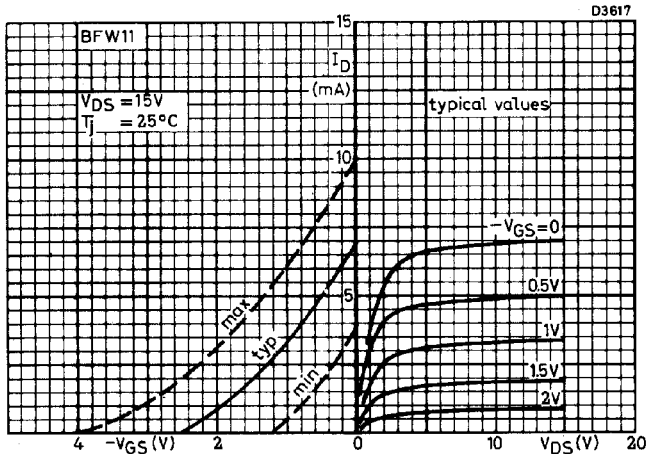
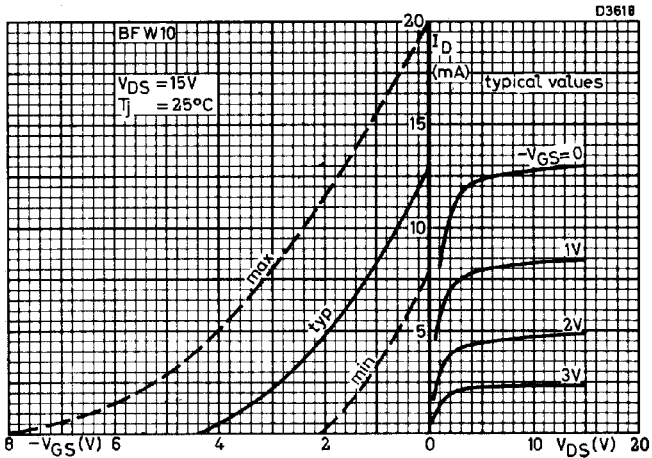
## ELECTRICAL CHARACTERISTICS (contd.)

		BFW10		BFW11	
$-V_{(P)GS}$	Gate-source cut-off voltage $I_D = 0.5nA, V_{DS} = 15V$	max.	8.0	6.0	V
y-parameters $V_{DS} = 15V, V_{GS} = 0, T_{amb} = 25^\circ C$					
f = 1.0kHz					
$ y_{fs} $	Transfer admittance	min.	3.5	3.0	mA/V
		max.	6.5	6.5	mA/V
$ y_{os} $	Output admittance	max.	85	50	$\mu A/V$
f = 1.0MHz					
$C_{is}$	Input capacitance	typ.	4.0	4.0	pF
		max.	5.0	5.0	pF
$-C_{rs}$	Feedback capacitance	typ.	0.6	0.6	pF
		max.	0.80	0.80	pF
f = 200MHz					
$ y_{fs} $	Transfer admittance	min.	3.2	3.2	mA/V
$g_{is}$	Input conductance	max.	800	800	$\mu A/V$
$g_{os}$	Output conductance	max.	200	100	$\mu A/V$
N	Noise figure, $T_{amb} = 25^\circ C$ f = 100MHz, $R_G = 1k\Omega$ $V_{DS} = 15V, V_{GS} = 0$ , (input tuned to minimum noise)	max.	2.5	2.5	dB
$V_n / \sqrt{B}$	Equivalent noise voltage $V_{DS} = 15V, V_{GS} = 0, f = 10Hz,$ $T_{amb} = 25^\circ C$	max.	75	75	$nV/\sqrt{Hz}$

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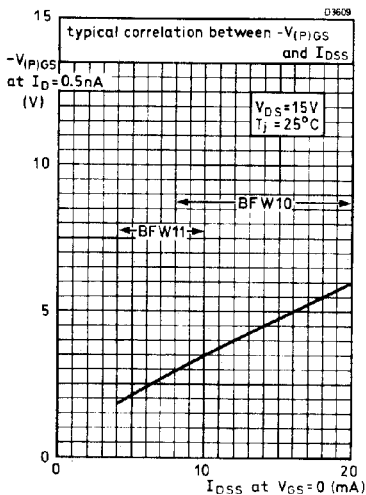
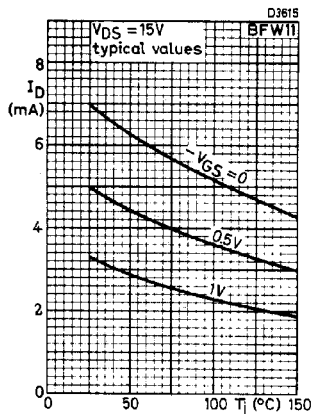
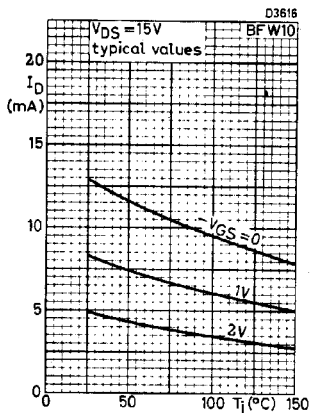
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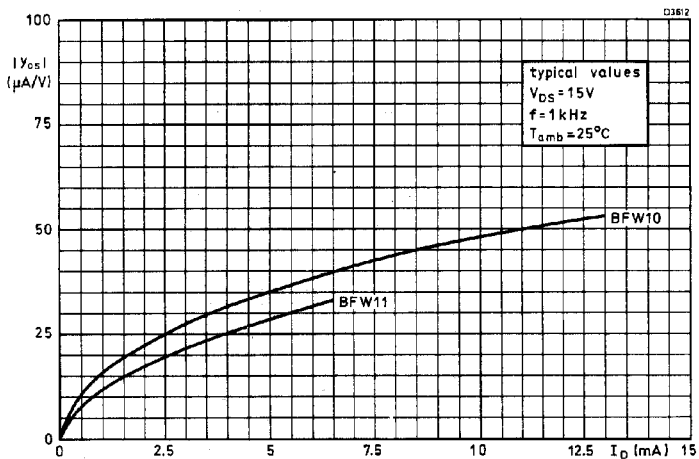
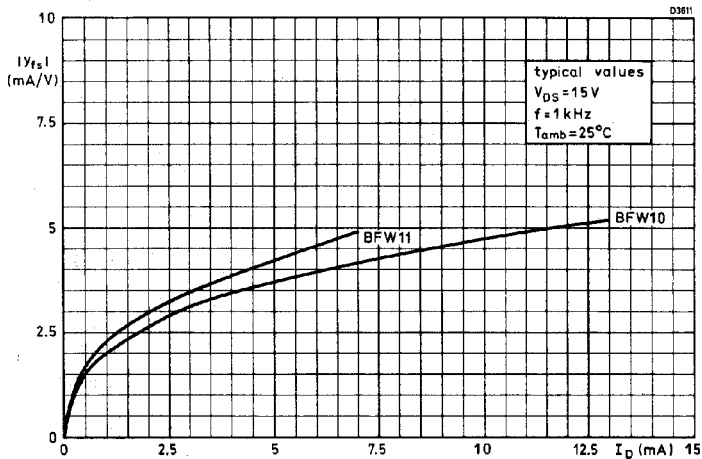


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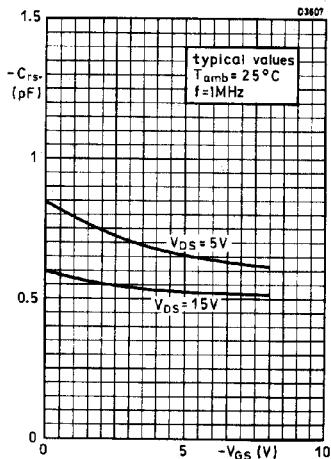
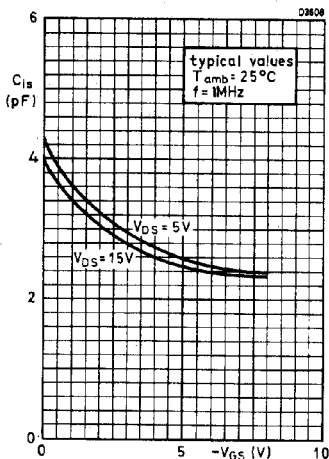


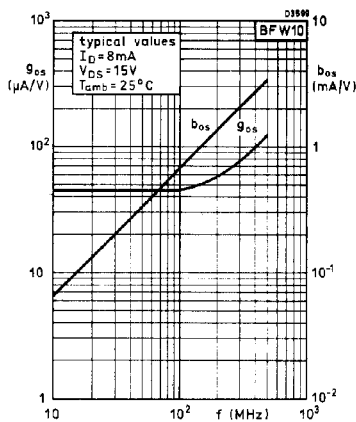
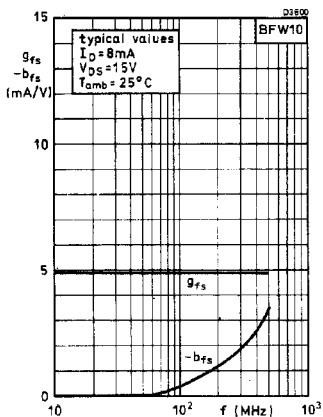
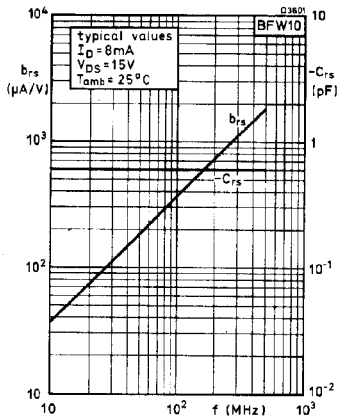
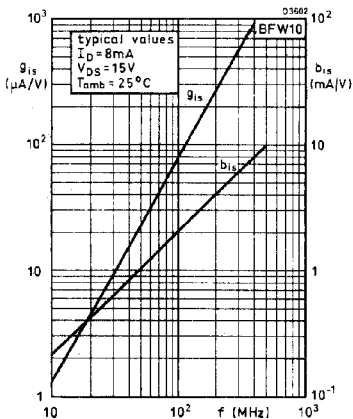
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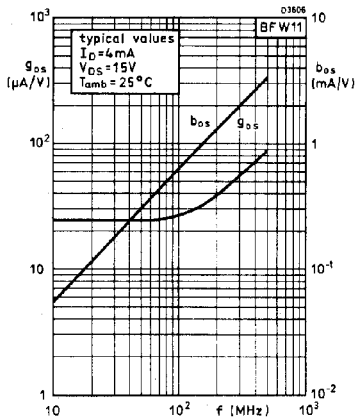
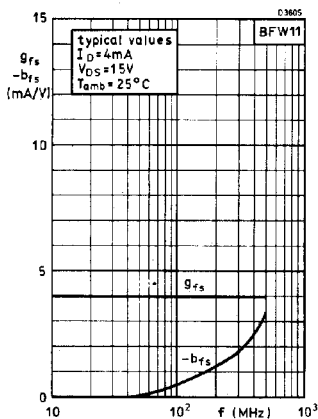
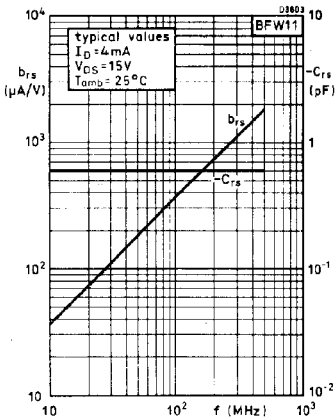
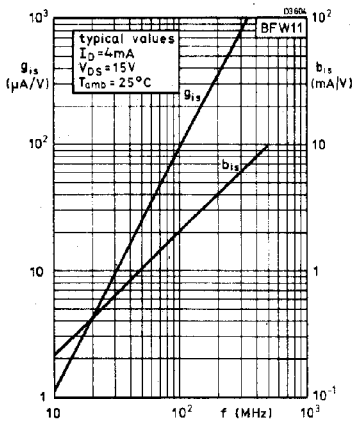
## BFW10 BFW11



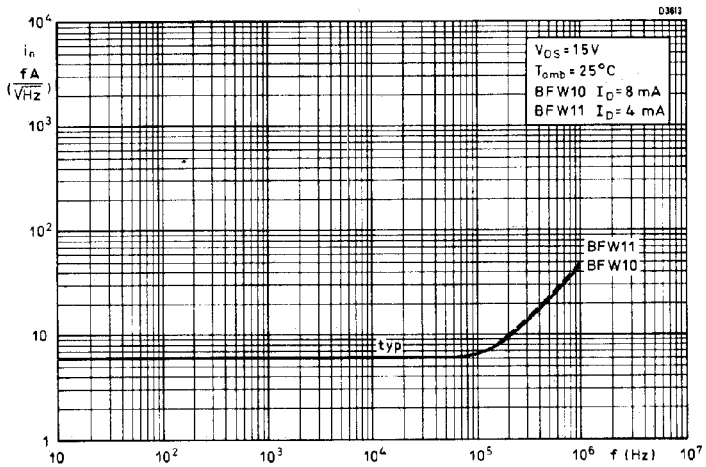
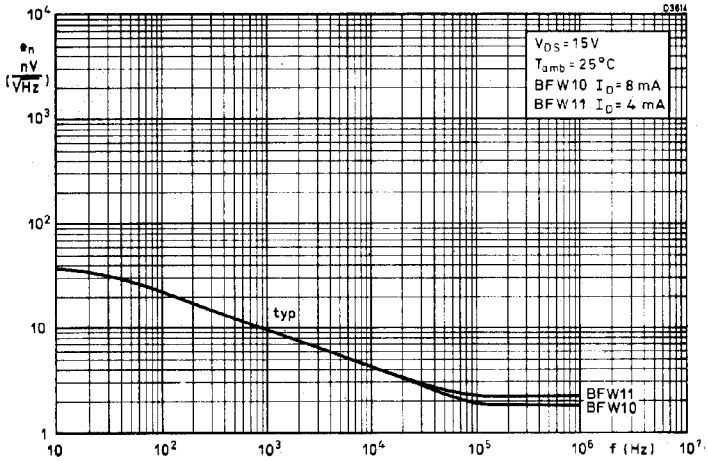


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# BFW10 BFW11

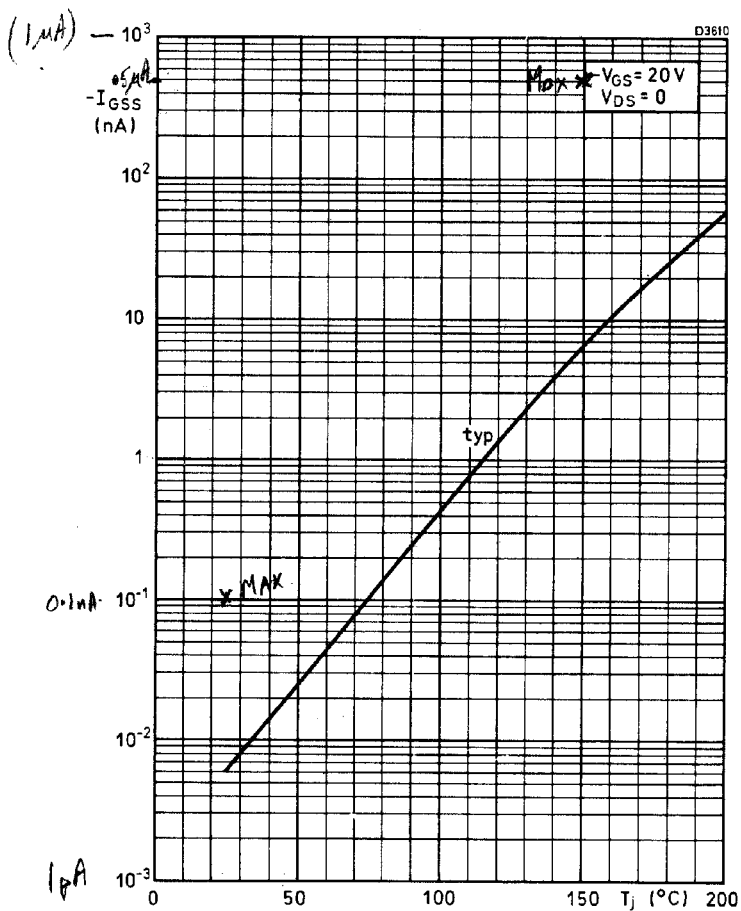


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**BFW10**  
**BFW11**



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